



501.39835CX1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: M. HASAGAWA, et al.  
Application No.: 10/627,769  
Filed: July 28, 2003  
For: A SEMICONDUCTOR MEMORY DEVICE HAVING  
IMPROVED ARRANGEMENT FOR REPLACING FAILED  
BIT LINES  
Art Unit: 2818  
Examiner: C. C. Yoha

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 22, 2004

Sir:

In response to the Office Action dated September 8, 2004, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims and**

**Remarks are included following the amendments.**